

# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/637,146	08/08/2003	Youngoo Yang	0140109	3406	
25700	7590 11/02/2004		EXAM	EXAMINER	
FARJAMI & FARJAMI LLP			NGUYEN, KHANH V		
	AMEDA AVENUE, SU EJO, CA 92691	ITE 360	ART UNIT PAPER NUMBER		
	•		2817	-	
			DATE MAILED: 11/02/2004	4	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/637,146	YANG ET AL.				
Office Action Summary	Examiner	Art Unit	i			
	Khanh V. Nguyen	2817	Bu			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).						
Status						
1) Responsive to communication(s) filed on <u>08 A</u>						
· <u> </u>	s action is non-final.					
,—	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
<ul> <li>4)  Claim(s) 1-20 is/are pending in the application.</li> <li>4a) Of the above claim(s) is/are withdrawn from consideration.</li> <li>5)  Claim(s) 8-13 is/are allowed.</li> <li>6)  Claim(s) 1,2,6,7,14,15,19 and 20 is/are rejected.</li> <li>7)  Claim(s) 3-5 and 16-18 is/are objected to.</li> <li>8)  Claim(s) are subject to restriction and/or election requirement.</li> </ul>						
Application Papers						
9)☐ The specification is objected to by the Examine	er.					
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
<ul> <li>12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No</li> <li>3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* See the attached detailed Office action for a list of the certified copies not received.</li> </ul>						
Attachment(s)  1) Notice of References Cited (PTO-892)  2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	ite	O-152)			

Application/Control Number: 10/637,146

Art Unit: 2817

## **DETAILED ACTION**

# **Drawings**

Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.121(d)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

# Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1, 6, 7, 14, 19, 20 are rejected under 35 U.S.C. 102(e) as being anticipated by Wang et al. (6,556,082).

Regarding claims 1, 14, Wang et al. (Fig. 2) disclose an amplifier circuit comprising: a first bipolar transistor (Q3) having a base connected to a first node (Va), said first node connected to a reference voltage (Vref) through a first resistor (Ra); a

second bipolar transistor (Q4) having a base connected to said first node (Va); a third bipolar transistor (Q2) having a collector connected to said first node (Va) and a base connected to an emitter of said first bipolar transistor (Q3) at a second node, an emitter of said second bipolar transistor (Qrf) being connected to a base of a fourth bipolar transistor (Qrf), said second bipolar transistor (Q4) not having a resistor connected to said emitter of the second bipolar transistor.

Regarding claims 6, 19, Wang et al. (Fig. 5) disclose resistor (R1) and diode (D1) can be read as a temperature compensation circuit which is connected in parallel with the first resistor (Ra) and having the inherent functions.

Regarding claims 7, 20, wherein the temperature compensation circuit comprises a resistor (R1) can be read as a second resistor having the connection as recited and that diode (D1) can be a Schottky diode having the cathode connected to the first node via transistor (Q1).

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 2, 15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Wang et al.

Wang et al. disclose the claimed invention except for the emitter size ratio. It would have been obvious matter of design choice to have changed the emitter size,

Art Unit: 2817

since such a modification would have involved a mere change in the size of a component. A change in size is generally recognized as being within the level of ordinary skill in the art.

# Allowable Subject Matter

Claims 8-13 are allowed.

Claims 3-5, 16-18 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

#### Reasons for Allowance

The following is an examiner's statement of reasons for allowance:

Claims 3-5, 8-13, 16-18 are allowed over the prior art because none of the prior art disclosed or suggested showing the particular structure and/or operation recited in these claims namely:

Claims 3-5, 8-13, 16-18 call for, among others, a control circuit or fifth transistor having the connections and functions thereof.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Application/Control Number: 10/637,146 Page 5

Art Unit: 2817

#### Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The additional reference (Miyazawa (6,331,799)) shows further analogous prior art circuitry.

This art is deemed relevant and should be carefully reviews before any amendment is filed.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khanh V. Nguyen whose telephone number is (571) 272-1767. The examiner can normally be reached from 8:00 AM - 3:30 PM EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571) 272-1769. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for After Final communications.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

KHANH V. NGUYEN
PRIMARY EXAMINER

Khowk and guyes